

AMENDMENTS TO THE SPECIFICATION

On page 1, please amend paragraph [003] as follows:

[003] It is often desired to run program code written for a computer processor of [[0]] a first type (a "subject" processor) on a processor of a second type (a "target" processor). Here, an emulator or translator is used to perform program code translation, such that the subject program is able to run on the target processor. The emulator provides a virtual environment, as if the subject program were running natively on a subject processor, by emulating the subject processor.

On pages 1-2, please amend paragraph [004] as follows:

[004] In the past, subject code is converted to an intermediate representation of a computer program during run-time translation using so-called base nodes, as described in co-pending patent application entitled Program Code Conversion, U.S. Patent Application Ser. No. 09/827,971, in connection with FIGS. [[I]]1 through 5 of this application. Intermediate representation is a term widely used in the computer industry to refer to forms of abstract computer language in which a program may be expressed, but which is not specific to, and is not intended to be directly executed on, any particular processor. Program code conversion methods and apparatus which facilitate such acceleration, translation and co-architecture capabilities utilizing intermediate representations are, for example, addressed in the co-pending patent application, U.S. Application Ser. No. 09/827,971.